



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of: Kelkar et al.

Attorney Docket No.: NSC1P181/ P04767

Application No.: 09/653,925

Examiner: Parekh, Nitin

Filed: September 1, 2000

Group: 2811

Title: Wafer Level Chip Scale Package

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on December 17, 2003 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

BEST AVAILABLE COPY

Signed:

Natalie Morgan
Natalie Morgan

AMENDMENT TRANSMITTAL

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an Amendment in the above-identified application.

The fee has been calculated as shown below.

	Claims After Amendment		Highest Previously Paid For	Present Extra	Small Entity Rate Fee	Large Entity Rate Fee
Total Claims	21	MINUS	21	0	x 9 =	x 18 = 0
Independent Claims	2	MINUS	2	0	x 42 =	x 86 = 0
Multiple Dependent Claim Present and Fee Not Previously Paid					\$140.00	\$280.00
Total					\$	\$0

- ☐ Applicant(s) hereby petition for a _____ month extension(s) of time to respond to the aforementioned Office Action.
- ☒ Applicant(s) believe that no (additional) Extension of Time is required; however, if it is determined that such an extension is required, Applicant(s) hereby petition that such an extension be granted and authorize the Commissioner to charge the required fees for an Extension of Time under 37 CFR 1.136 to Deposit Account No. 500388.
- ☒ Enclosed is our Check No. 8384 in the amount of \$770.00 to cover the RCE fee.
- ☒ Please charge the required fees, or any additional fees required to facilitate filing the enclosed response, to Deposit Account No. 500388 (Order No. NSC1P181).

Respectfully submitted,
BEYER WEAVER & THOMAS, LLP

Desmund Gean

Desmund Gean
Reg. No. 52,937

P.O. Box 778
Berkeley, CA 94704-0778

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: KELKAR et al.

Attorney Docket No.: NSC1P181/P04767

Application No.: 09/653,925

Examiner: Parekh, Nitin

Filed: September 1, 2000

Group: 2811

Title: WAFER LEVEL CHIP SCALE PACKAGE

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Printed Name: _____ Natalie Morgan _____

Signed: Natalie Morgan

AMENDMENT D

(submitted with an RCE herewith)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir or Madame:

In response to the Office Action dated September 17, 2003, a response to which is due December 17, 2003, please enter the following amendments and consider the following remarks:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

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